

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 07-322179
 (43)Date of publication of application : 08.12.1995

(51)Int.Cl. H04N 5/66
 H04N 5/66
 H04N 5/57

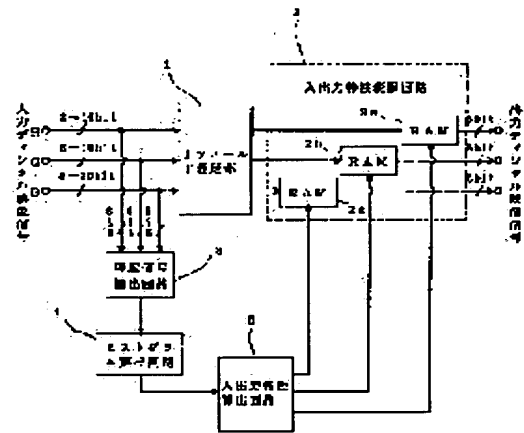
(21)Application number : 06-136310 (71)Applicant : FUJITSU GENERAL LTD
 (22)Date of filing : 26.05.1994 (72)Inventor : SUGAWARA MOTOO

(54) VIDEO DISPLAY PROCESSING METHOD FOR ELECTRONIC DISPLAY AND ITS DEVICE

(57)Abstract:

PURPOSE: To suppress the black and white collapses of a screen, to improve the contrast of the screen and to improve image quality when video is displayed on electronic displays such as an LCD and a PDP, etc.

CONSTITUTION: This method and device are a video display processing method and its device in which video is possible to be displayed by performing an input/output characteristic conversion for an input video signal according to the characteristic of an electronic display. A one-field delay is performed for an input video signal (RGB digital video signal) in a one-field delay part 1 and the luminance signal of the input video signal is detected in a luminance signal detection circuit 3. By the luminance signal, the same signal level is counted in a histogram calculation circuit 4 and the histogram of the video signal is prepared for every field. In an input/output characteristic calculation circuit 5, the input/output characteristic data of an input/output characteristic conversion circuit 2 according to the average value (APL : average video level) and the dispersion (or standard deviation) of the histogram is calculated. The sum total of the histogram is calculated, it is added to the input/output characteristic data by a prescribed ratio and the final input/output characteristic data of the input/output characteristic conversion circuit 2 is obtained.



LEGAL STATUS

[Date of request for examination]
 [Date of sending the examiner's decision of rejection]
 [Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]
 [Date of final disposal for application]
 [Patent number]
 [Date of registration]
 [Number of appeal against examiner's decision of rejection]
 [Date of requesting appeal against examiner's decision of rejection]
 [Date of extinction of right]

Copyright (C); 1998,2000 Japanese Patent Office

BEST AVAILABLE COPY

公開特許公報フロントページ

(11)公開番号：特開平07-322179

(43)公開日：1995年12月08日

(51)Int.Cl6

H04N 5/66 102
5/57 101

(21)出願番号：特願平06-136310

(71)出願人：株式会社富士通ゼネラル

(22)出願日：1994年05月26日

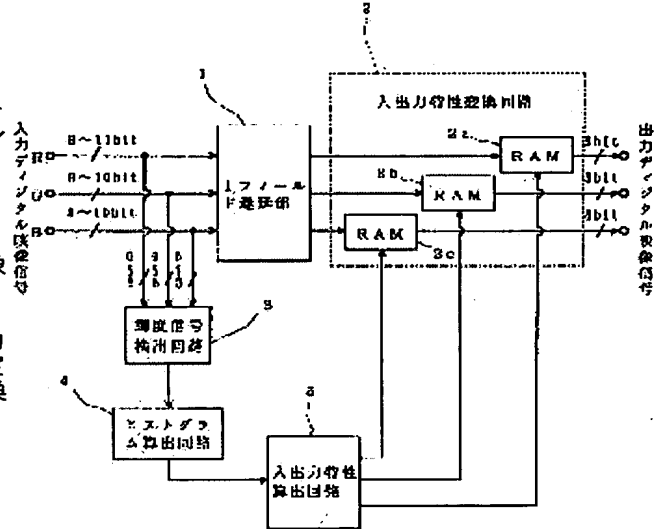
(72)発明者：菅原 元雄

(54) 電子ディスプレイの映像表示処理方法およびその装置

(57)【要約】

【目的】LCDやPDP等の電子ディスプレイに映像を表示する際、画面の黒つぶれ、白つぶれを抑え、かつ画面のコントラストを改善し、画質の向上を図る。

【構成】入力映像信号を電子ディスプレイの特性に合わせて入出力特性変換して映像を表示可能とする電子ディスプレイの映像表示処理方法およびその装置であって、入力映像信号(RGBデジタル映像信号)を1フィールド遅延部1で1フィールド遅延する一方、その入力映像信号の輝度信号を輝度信号検出回路3で検出し、この輝度信号により同じ信号レベルをヒストグラム算出回路4でカウントして1フィールド毎に映像信号のヒストグラムを作成し、入出力特性算出回路5でそのヒストグラムの平均値(APL;平均映像レベル)および分散(または標準偏差)に応じた入出力特性変換回路2の入出力特性データを算出し、またヒストグラムの累和を算出し、所定の割合で先の入出力特性データに加算して入出力特性変換回路2の最終的な入出力特性データを得る。



リーガルステータス

【審査請求日】

【拒絶査定発送日】

【最終処分種別】

【最終処分日】

【特許番号】

【登録日】

【拒絶査定不服審判番号】

【拒絶査定不服審判請求日】

【本権利消滅日】

Copyright (C); 1998,2000 Japanese Patent Office